Dual CPU configuration	on						
			DIMMs				
	2	4	8	12	16	24	32
CPU1_DIMM_A2						•	•
CPU1_DIMM_A1	•	•	•	•	•	•	•
CPU1_DIMM_B2							•
CPU1_DIMM_B1					•	•	•
CPU1_DIMM_C2						•	•
CPU1_DIMM_C1			•	•	•	•	•
CPU1_DIMM_D2							•
CPU1_DIMM_D1				•	•	•	•
CPU1_DIMM_H1					•	•	•
CPU1_DIMM_H2					1		•
CPU1_DIMM_G1		•	•	٠	•	•	•
CPU1_DIMM_G2					1	•	•
CPU1_DIMM_F1				٠	•	•	•
CPU1_DIMM_F2							•
CPU1_DIMM_E1			•	•	•	•	•
CPU1_DIMM_E2						•	•
CPU2_DIMM_A2						•	•
CPU2_DIMM_A1	•	•	•	•	•	•	•
CPU2_DIMM_B2							•
CPU2_DIMM_B1					•	•	•
CPU2_DIMM_C2						•	•
CPU2_DIMM_C1			•	•	•	•	•
CPU2_DIMM_D2							•
CPU2_DIMM_D1				٠	•	•	•
CPU2_DIMM_H1					•	•	•
CPU2_DIMM_H2							•
CPU2_DIMM_G1		•	•	•	•	•	•
CPU2_DIMM_G2					1	•	•
CPU2_DIMM_F1				•	•	•	•
CPU2_DIMM_F2							•
CPU2_DIMM_E1			•	•	•	•	•
CPU2_DIMM_E2						•	•

Recommended memory configuration for 2 CPU Configuration